EPS13D2 Series

- RoHS Compliant (Pb-Free)
- EPS[™] Spread Spectrum Programmable Clock Oscillators
- Ceramic 4-pad SMD Package
- Low EMI LVHCMOS Output
- 3.3V Supply Voltage
- Stability to 100ppm
- Center Spread
- Tri-State and
- Available on T

ELECTRICAL SPECI

 Center Spread and Down Spread Modulation Tri-State and Power Down Options Available 		W 5.0	
• Available on Tape	& Reel		OSCILLATOR
ELECTRICAL SPECIFICA	TIONS		
Nominal Frequency			14.318MHz to 166.000MHz
Operating Temperature Range			-20°C to 70°C
Storage Temperature Range			-55°C to 125°C
Supply Voltage (V _{DD})			3.3V _{DC} ±0.3V _{DC}
Maximum Supply Voltage			$-0.5V_{DC}$ to $7.0V_{DC}$
Input Current	$IInloaded \cdot V_{nn} = 3.3V_{nn}$		30mA Maximum

Supply vollage (vol)		J.J V DC TO.J V DC
Maximum Supply Voltage		$-0.5V_{DC}$ to $7.0V_{DC}$
Input Current	Unloaded; $V_{DD} = 3.3V_{DC}$	30mA Maximum
Frequency Tolerance / Stability	Inclusive of All Conditions: Frequency Stability over the	±100ppm Maximum
	Operating Temperature Range, Supply Voltage Change,	
	Output Load Change, 1st Year Aging at 25°C, Shock, and	
	Vibration	
Output Voltage Logic High (V _{OH})	$I_{OH} = -8mA$	V_{DD} -0.4 V_{DC} Minimum
Output Voltage Logic Low (V _{oL})	$I_{OL} = +8mA$	0.4V _{DC} Maximum
Rise Time / Fall Time	20% to 80% of waveform	2.7nSeconds Maximum
Duty Cycle	at 50% of waveform	50 ±10(%)
		50 ±5(%)
Load Drive Capability		15pF HCMOS Load Maximum
Output Control Function	Internal Pull Down Resistor of 100kOhms Typical on Pad 3,	Tri-State or Power Down
	Internal Pull Up Resistor of 100k0hms Typical on Pad 1	
Tri-State/Power Down Input Voltage	V_{IH} of 70% of V_{DD} Minimum	Enables Output
	No Connection	Enables Output
	V_{IL} of 30% of V_{DD} Maximum	Disables Output: High Impedance
Power Down Output Disable Time		350nSec Maximum
Power Down Output Enable Time		3mSec Maximum
Standby Current	Unloaded; Pad 1 = Ground; $V_{DD} = 3.3V_{DC}$	50µA Maximum
Tri-State Output Disable Time		350nSec Maximum
Tri-State Output Enable Time		350nSec Maximum
Disable Current	Unloaded; Pad 1 = Ground; $V_{DD} = 3.3V_{DC}$	20mA Maximum
Spread Spectrum Percentage	$\pm 0.25\%, \pm 0.50\%, \pm 0.75\%, \pm 1.0\%, \pm 1.5\%, \pm 2.0\%$	Center Spread

-0.50%, -1.0%, -1.5%, -2.0%, -3.0%, -4.0%

Cycle to Cycle; Spread Spectrum-On; $V_{DD} = 3.3 V_{DC}$

PACKAGE CERAMIC



Period Jitter

Aging

Start Up Time

MANUFACTURER ECLIPTEK CORP. CATEGORY OSCILLATOR

800-ECLIPTEK www.ecliptek.com for latest revision

SERIES EPS13D2

First Year at 25°C

Specifications subject to change without notice.

VOLTAGE

3.3V

Down Spread

33kHz Maximum

±5ppm Maximum

10mSec Maximum

30kHz Minimum, 31.5kHz Typical,

400pSec Maximum 25.000MHz to 133.000MHz

REV DATE 09/07

700pSec Maximum < 25.000MHz

300pSec Maximum > 133.000MHz

CLASS

0S2G





PART NUMBERING GUIDE

